

3/15/03
14/11/03

Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED <i>KCS</i> Q.A. <i>[Signature]</i>	PATENT DATE
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APPLICATION NO. 09/733036	CONT/PRIOR D F	CLASS 372	SUBCLASS 109	ART UNIT 2881	EXAMINER Leung
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APPLICANTS
TITLE

Kenji Yamauchi

Semiconductor laser diode chip and its positoning and mouting method

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL			CROSS REFERENCE(S)						
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION									

☐ Continued on Issue Slip inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	

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